

Method and apparatus for branching when a program is executed by a processor

A method for branching when a program is executed by a processor, where the program is stored in a program memory, and a variable memory and a table memory are provided, contains the following steps:

- a) a first memory cell in the variable memory is addressed,
- b) a second memory cell in the variable memory is addressed on the basis of the content of the first memory cell, addressed in step a), and further parameters,
- c) a memory cell in the table memory is addressed on the basis of the content of the second memory cell, addressed in step b), and
- d) execution branches to a program address which is stored in that memory cell in the table memory which was addressed in step c).

[Figure 1]